

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: J-S. Mok et al. Attorney Docket No.: LEPA122042  
Application No.: 10/717,977 Art Unit: 2822 / Confirmation No: 8002  
Filed: November 20, 2003 Examiner: M.M. Trinh  
Title: PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD  
HAVING IMPROVED INTERCONNECTION AND METHOD  
FOR MANUFACTURING THE SAME

**AMENDMENT AFTER FINAL**

Seattle, Washington 98101

April 25, 2007

TO THE COMMISSIONER FOR PATENTS:

**INTRODUCTORY COMMENTS**

Please amend the above-identified application as follows.

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